

QUESTION

Figure 1 shows a circuit diagram of a two-port network. The input port is connected to a voltage source V_s and a resistor R_s . The output port is connected to a load resistor R_L . The network consists of a resistor R_1 in series with a parallel combination of a resistor R_2 and a dependent current source βI_1 . The current I_1 is the current through R_1 . The output voltage is V_o and the output current is I_o .



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